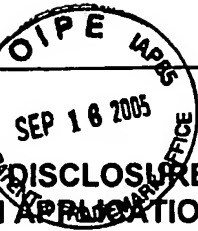


FORM PTO-1449



INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)
81839.0107Application Number
10/009,910

Applicant

IIDA, et al.

Filing Date

December 12, 2001

Group Art Unit

1722

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
MS	2001-274166	10/05/01	JAPAN			Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

MS	"MOS Device Epitaxial Wafer", edited by Hirota Sumitomo, Realize Limited 6/30/98, pgs. 13-15

EXAMINER

MSJ

DATE CONSIDERED

12/12/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.